

☒ Pending
☒ Active
 L7: (12) ((multiple multi) adj (bank block array)) with macro
 L8: (9) L7 and ((bank block array) near3 select\$3)
 L9: (8) L7 and (simultaneous\$2 concurrent\$2)
 L10: (8) L8 and L9
 L11: (1) L10 and ((bank block array) near3 select\$3 near3 pi
☒ Failed
☒ Saved
 (29) ((select\$3 near3 bank) same (input near3 control\$4)) and
 (9) ((select\$3 near3 bank) with (input near3 control\$4)) and
 (0) (((select\$3 near3 bank) with (input near3 control\$4)) and
 (0) ((select\$3 near3 bank) same (input near3 control\$4)) and
 (11) ("6128704" "5835932" "5787457" "5998251" "6064588" "606
 (11) ("6128704" "5835932" "5787457" "5998251" "6064588" "60
 (0) 4845713.PN. and spare
 (0) 4845713.PN. and spar\$3
 (1424) dram and spar\$3 and redundan\$3
 (9) (dram and spar\$3 and redundan\$3) and bank and macro and
 (330) (dram and spar\$3 and redundan\$3) and (spar\$3 with row
 (4) ((dram and spar\$3 and redundan\$3) and (spar\$3 with row
 (158) (dram and spar\$3 and redundan\$3) and (fall\$3 near3 edge
 (126) ((dram and spar\$3 and redundan\$3) and (fall\$3 near3 edge
 (28) (dram and spar\$3 and redundan\$3) and ((fall\$3 near3 edge
 (22) dram and (((fall\$3 near3 edge) with latch\$3) same (bank
 (0) page adj mode adj select adj input
 (10) (page adj2 mode) with (select near2 input)
 (17) ("6128704" "5835932" "5787457" "5998251" "6064588" "606
 (3) dram and (master adj select adj input)
 (5) dram and ((master adj select) with input)

DB: USPAT; US POPUP; EPO; JPO; DERWENT; IBM; TDB
 Default operator: OR
 L8 and L9

☒ RIS term ☒ RIS term ☒ Image ☒ Text ☒ HTML

	U		Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C					
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6418067 B1	20020709	77	Semiconductor memory device suitable for merging with	365/200	365/230.03		Watanabe, Naoya et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6381671 B1	20020430	33	Semiconductor integrated circuit and data processing	711/104	711/100; 711/118;		Ayukawa, Kazushige et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6310815 B1	20011030	59	Multi-bank semiconductor memory device suitable for	365/230.03	365/51; 365/63		Yamagata, Tadato et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	US 20030194145 A1	20031016	28	Method and apparatus for encoding image data, and	382/243	382/239; 382/251		Chiba, Hirotsuka et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	US 20030123278 A1	20030703	7	Flexible multibanking interface for embedded	365/149			Anand, Darren L. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input type="checkbox"/>	US 20020103961 A1	20020801	34	Semiconductor integrated circuit and data processing	711/104	711/118; 711/158		Ayukawa, Kazushige et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input type="checkbox"/>	US 4477596 A	19841016	11	Amphoteric ion exchanger and method of making same	521/25	521/27; 521/38		Fujimoto, Teruo	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input type="checkbox"/>	US 20030123278 A	20030703	7	Multi-bank DRAM (dynamic RAM) macro for logic device				ANAND, D L et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>